

NE 555 P PDIP-8 Timer IC

Slug: ne-555-p-pdip-8-timer-ic-pack-of-5



TECHNICAL SPECIFICATIONS & SUMMARY

[Timer,Clock,Counter ICs] The NE 555 P timer IC is a precision timing circuit capable of producing accurate time delays or oscillation. In the time-delay or monostable mode of operation, the timed interval is controlled by a single external resistor and capacitor network. In the as table mode of operation, the frequency and duty cycle can be controlled independently with two external resistors and a single external capacitor. The threshold and trigger levels normally are two-thirds and one-third, respectively, of VCC. These levels can be altered by the use of the control voltage terminal. When the trigger input falls below the trigger level, the flip-flop is set, and the output goes high. If the trigger input is above the trigger level and the threshold input is above the threshold level, the flip-flop is reset and the output is low. Features: Timing from microseconds to hours A stable or monostable operation Adjustable duty cycle TTL compatible output can sink or source up to 200mA Applications: Clock & Timing, Consumer Electronics, Aerospace, Defence, Military, Embedded Design & Development Package

UNIT PRICE (EX. GST)

Rs. 10

WITH 18% GST

Rs. 12

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