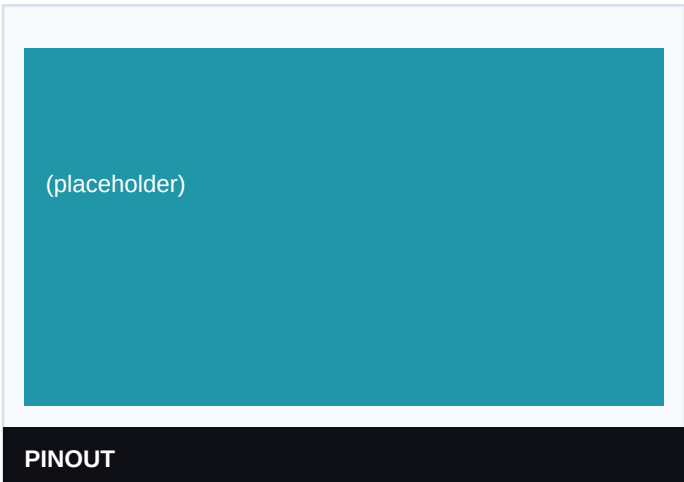
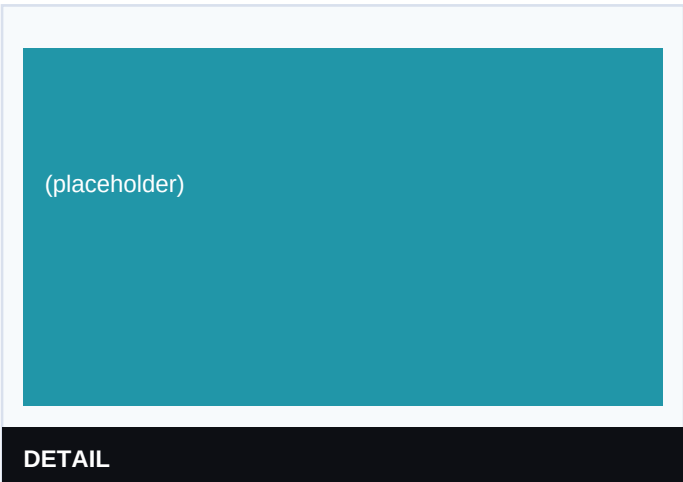
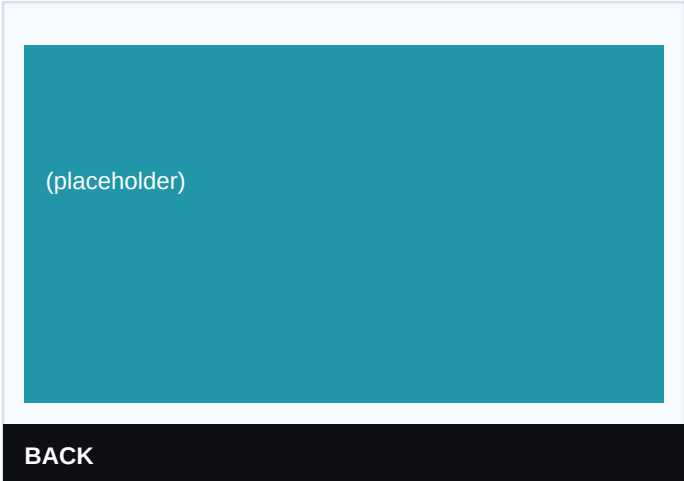
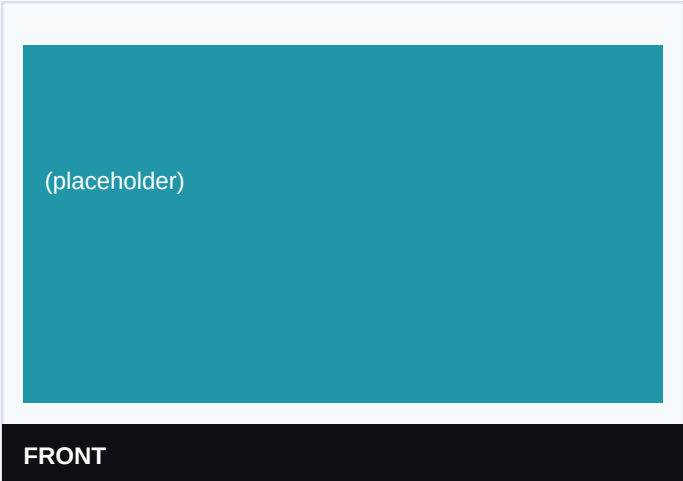


74HC20 Dual 4-input NAND Gate IC (7420 IC) DIP-14 Package

Slug: 74hc20-dual-4-input-nand-gate-ic-7420-ic-dip-14-package



TECHNICAL SPECIFICATIONS & SUMMARY

[Logic Gates IC] The 74HC20 is a 14 Pin Quad 4-Input NAND Gate IC. This device contains four independent gates each of which performs the logic NAND function. NAND gates utilize advanced silicon-gate CMOS technology to achieve operating speeds similar to LS-TTL gates with the low power consumption of standard CMOS integrated circuits. All gates have buffered outputs. All devices have high noise immunity and the ability to drive 10 LS-TTL loads. The 74HC logic family is functionally as well as pin-out compatible with the standard 74LS logic family. All inputs are protected from damage due to static discharge by internal diode clamps to VCC and ground. Specifications :- Parameter Specification Supply Voltage (VCC) 0.5 to + 7.0V DC Input Voltage (VIN) 0.5V to VDD + 0.5V Output Voltage (VOUT) 0.5 to VCC + 0.5V Clamp Diode Current (IHK, IOK) +/- 20 mA DC Output Current, per pin (IOUT) +/- 25 mA DC VCC or GND Current, per pin (ICC) +/- 50 mA Storage Temperature Range (TSTG) 65 deg C to + 150 deg C Power Dissipation (PD) (Note 4) 500 mW S.O. Package only 500 mW

UNIT PRICE (EX. GST)

Rs. 15

WITH 18% GST

Rs. 18

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